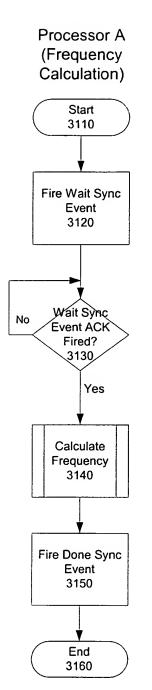


Figure 1



Processor B (Known Code Synchronization and Running)

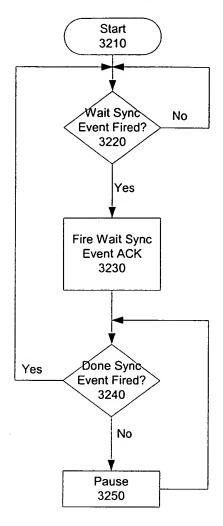


Figure 2

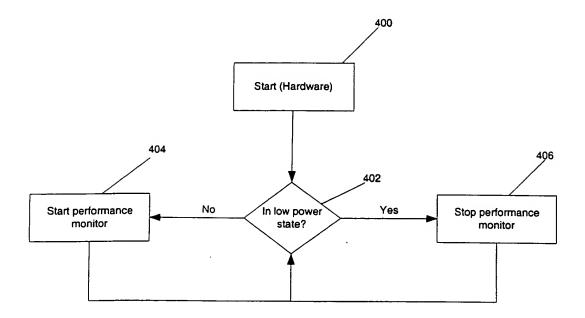


Figure 3

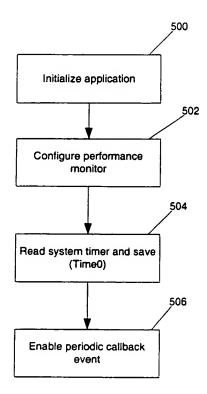


Figure 4

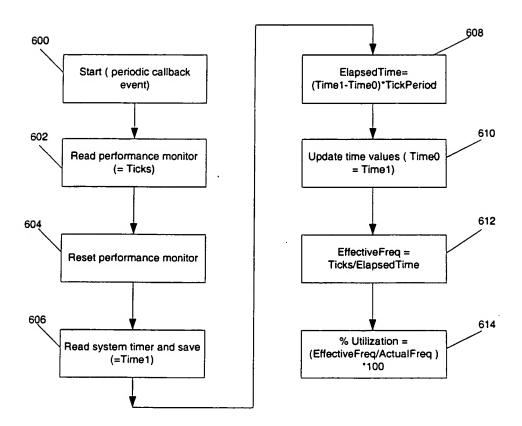


Figure 5

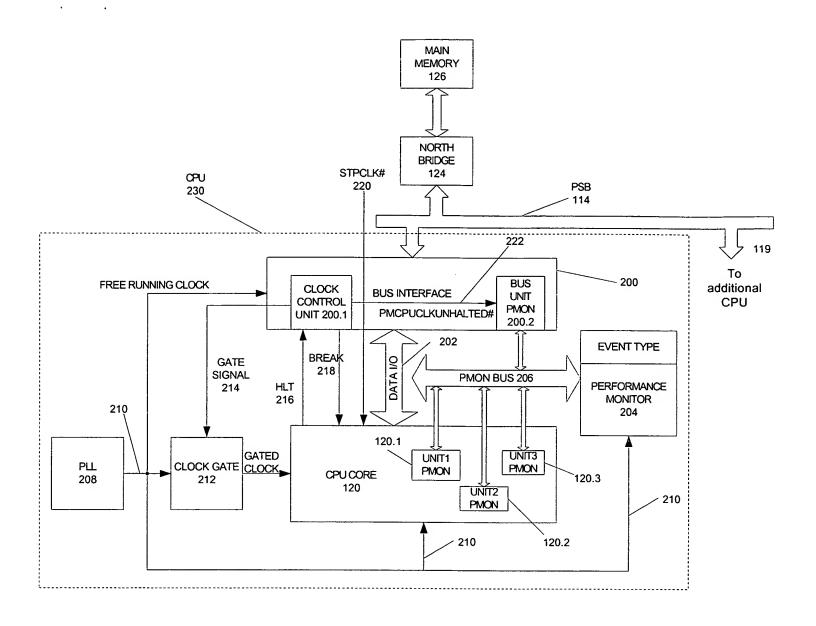
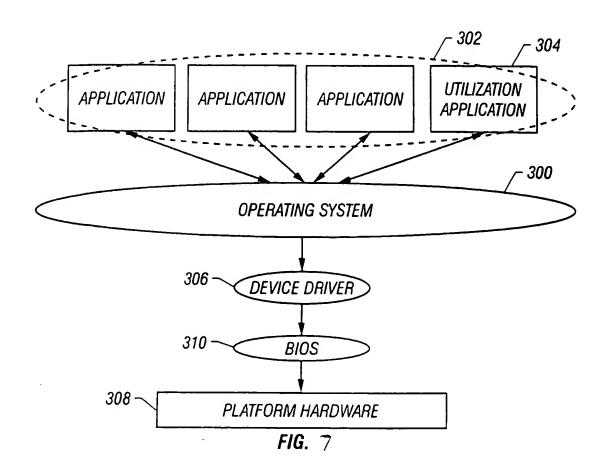


FIGURE 6



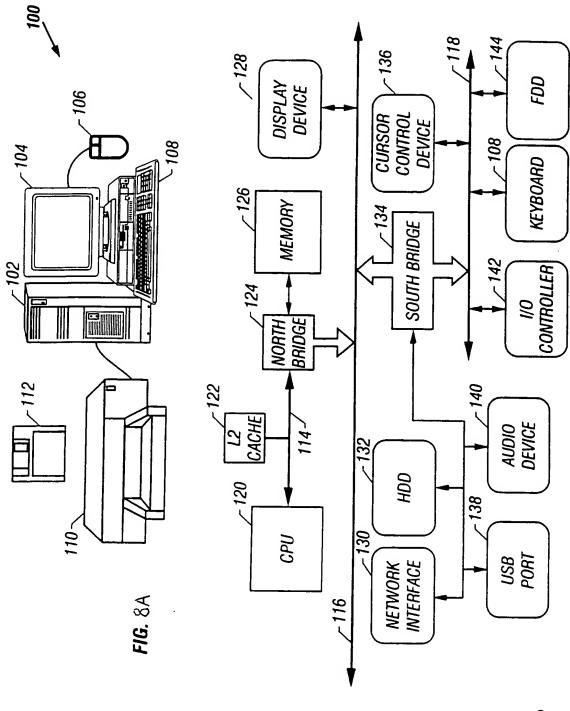


FIG. 8B